TOSHIBA 00256060

FIG. 1

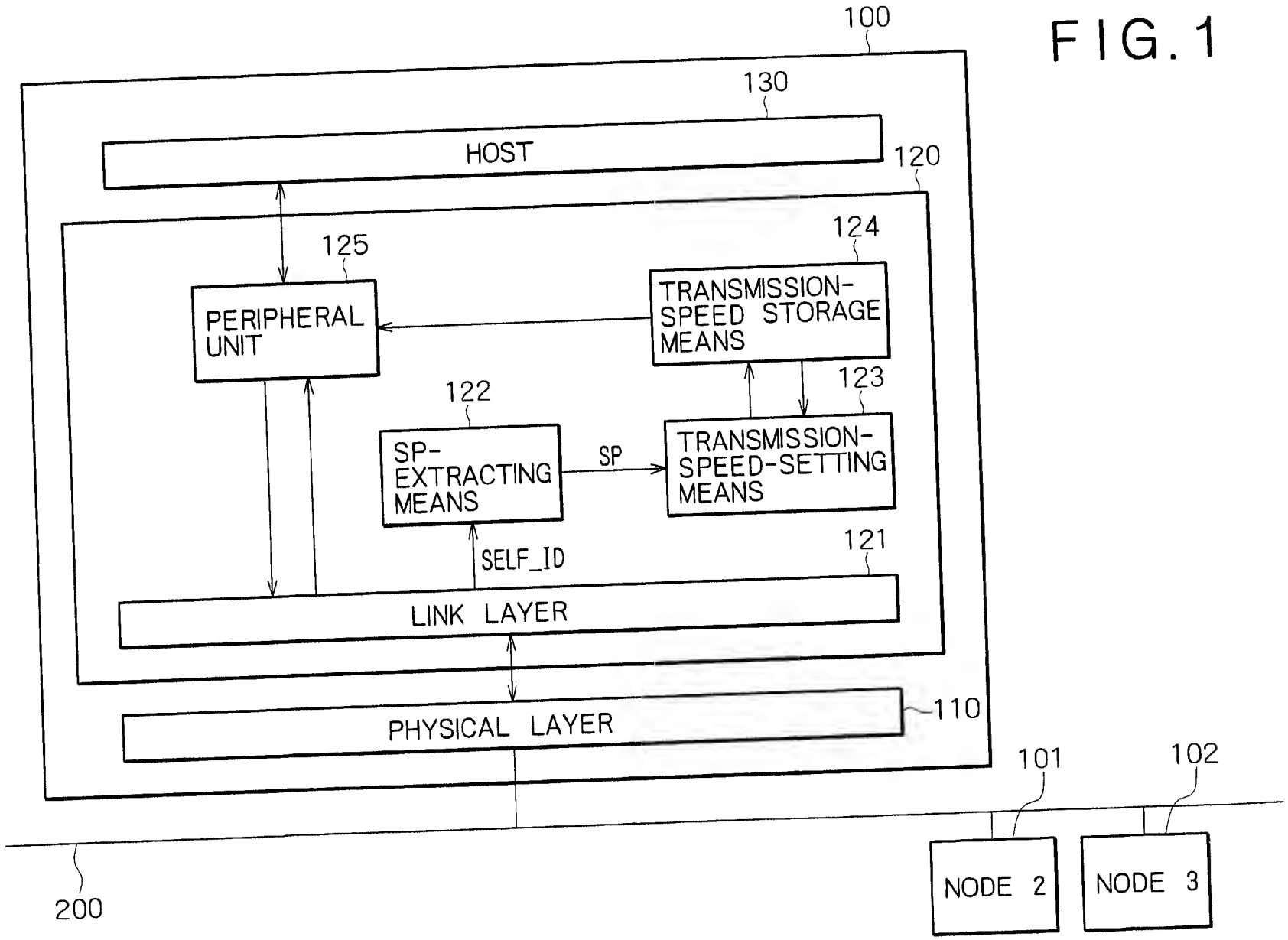

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FIG. 2

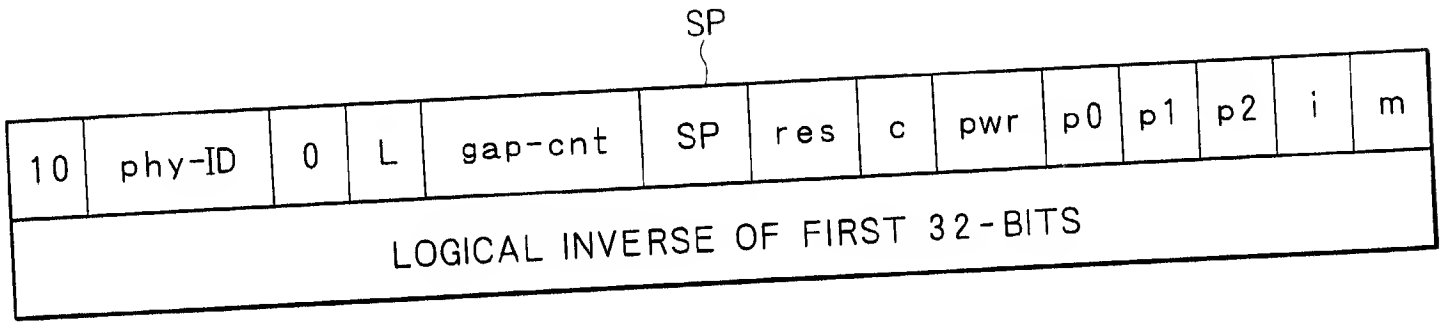

SP
10
phy-ID
0
L
gap-cnt
SP
res
c
pwr
p0
p1
p2
i
m
LOGICAL INVERSE OF FIRST 32-BITS

FIG. 3

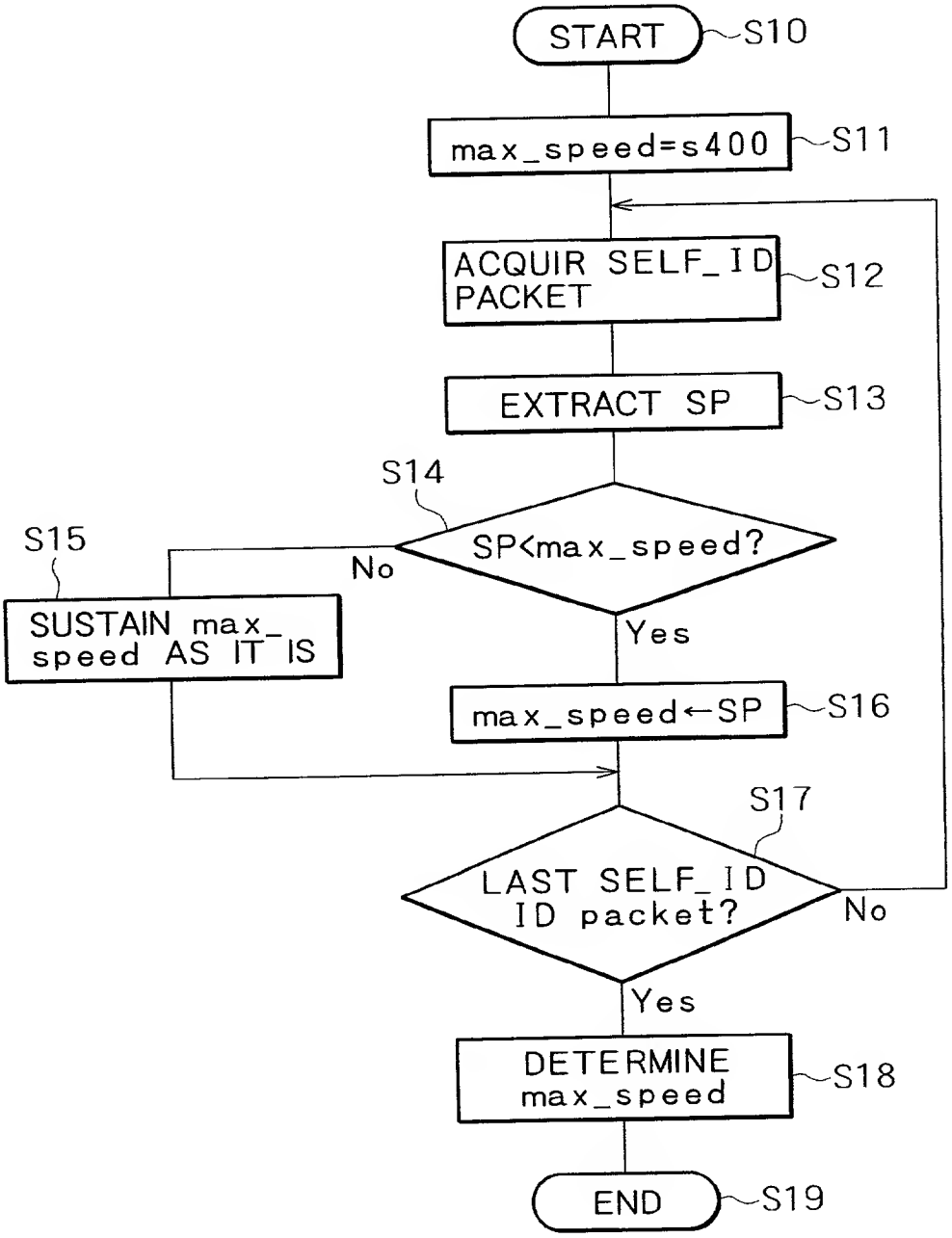

START
S10
max_speed=s400
S11
ACQUIR SELF_ID PACKET
S12
EXTRACT SP
S13
S14
SP<max_speed?
No
S15
SUSTAIN max_speed AS IT IS
Yes
max_speed←SP
S16
S17
LAST SELF_ID ID packet?
No
Yes
DETERMINE max_speed
S18
END
S19

FIG. 4

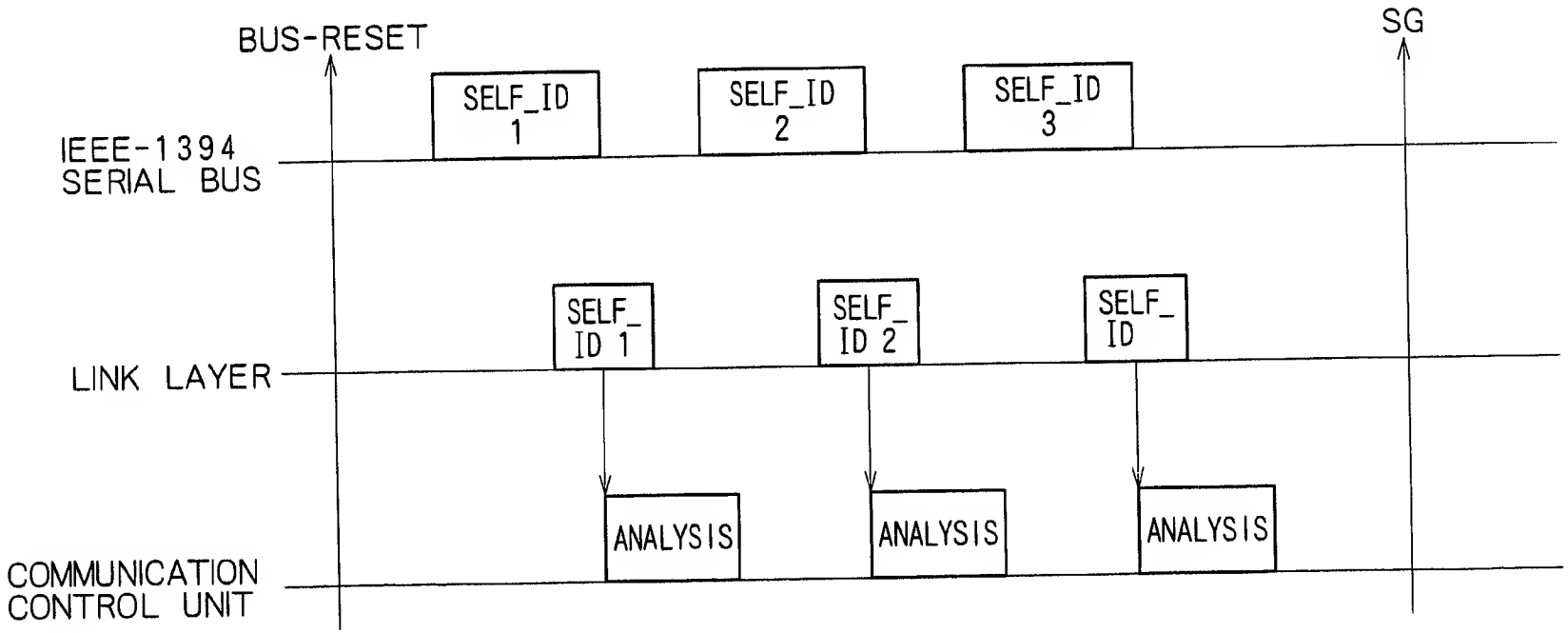
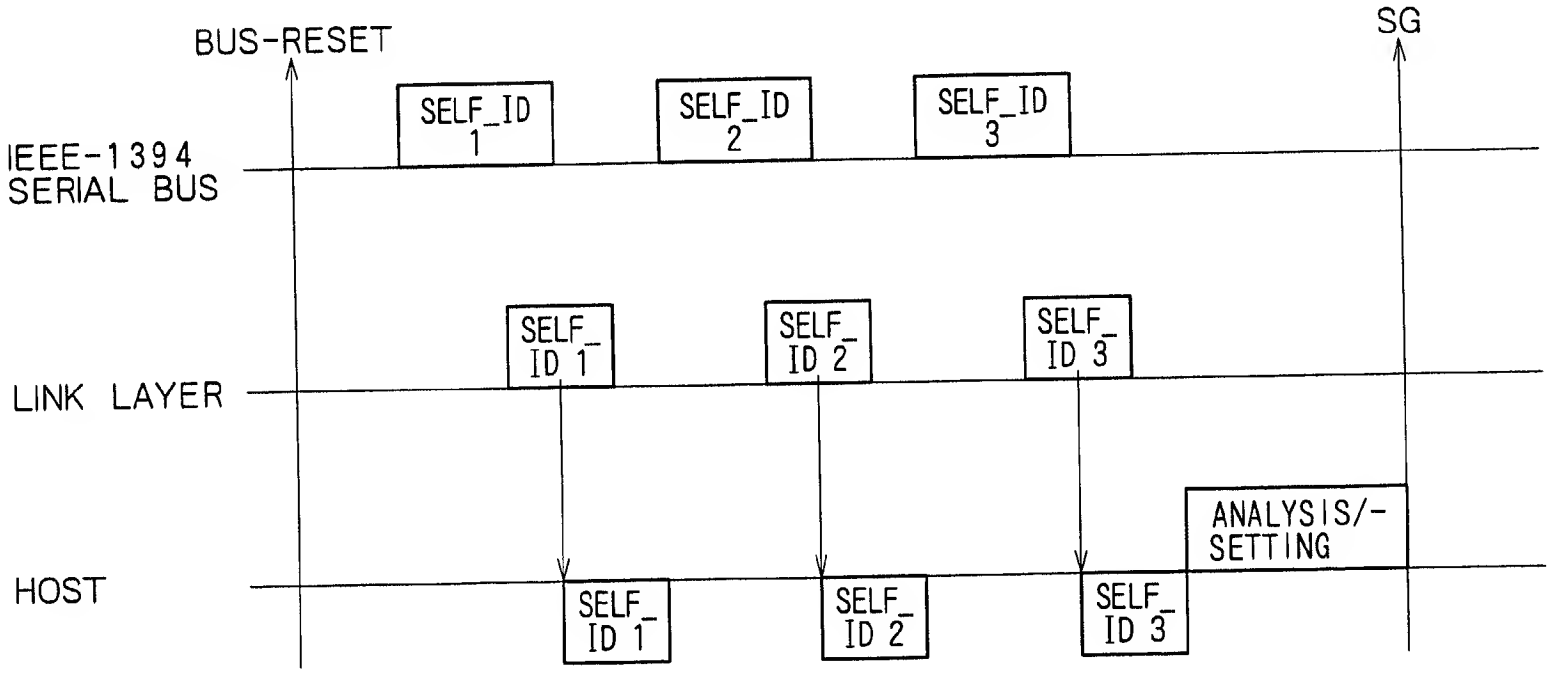

BUS-RESET
SG
SELF_ID 1
SELF_ID 2
SELF_ID 3
IEEE-1394 SERIAL BUS
SELF_ID 1
SELF_ID 2
SELF_ID
LINK LAYER
ANALYSIS
ANALYSIS
ANALYSIS
COMMUNICATION CONTROL UNIT

FIG.5


BUS-RESET
SG
SELF_ID 1
SELF_ID 2
SELF_ID 3
IEEE-1394 SERIAL BUS
SELF_ID 1
SELF_ID 2
SELF_ID 3
LINK LAYER
ANALYSIS/-SETTING
HOST
SELF_ID 1
SELF_ID 2
SELF_ID 3